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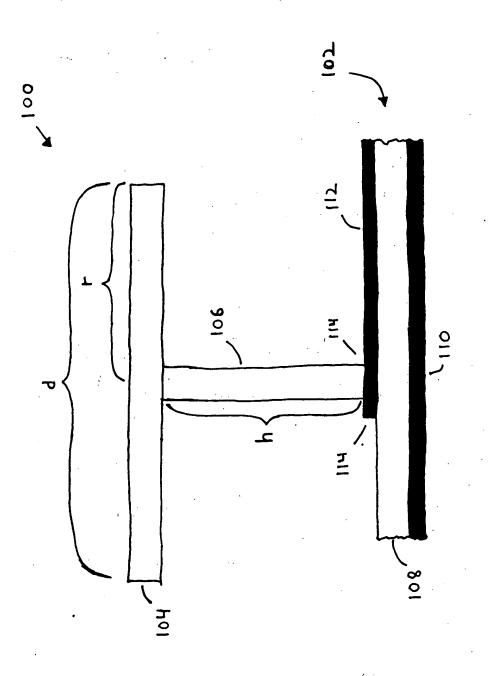
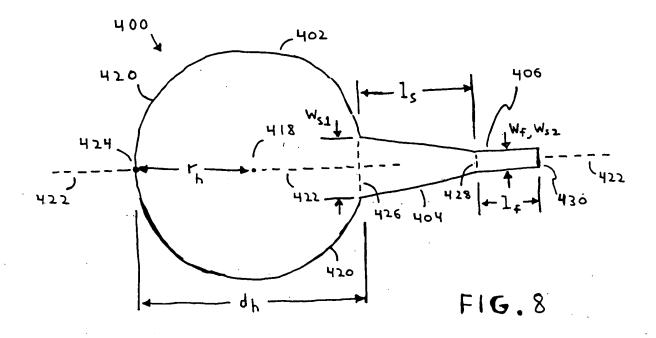
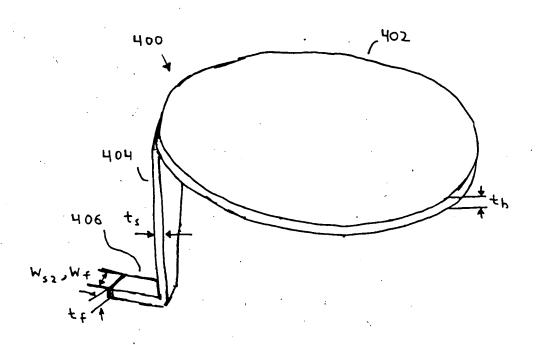
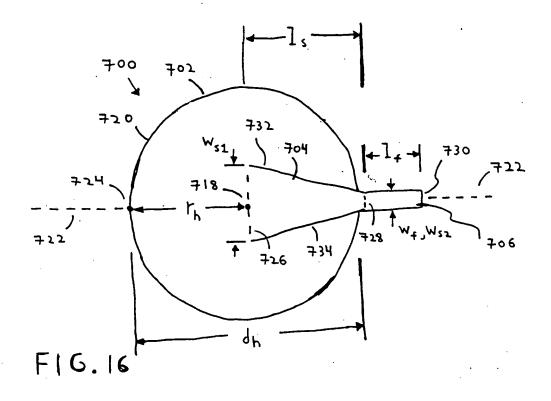


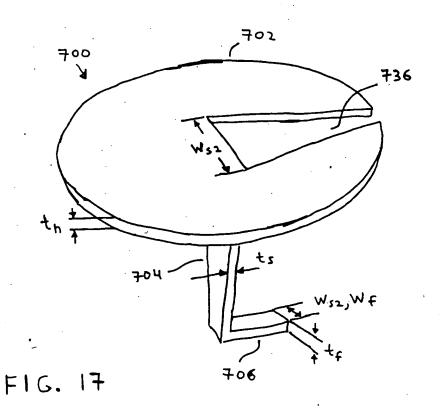
FIG. 1 PRIOR ART

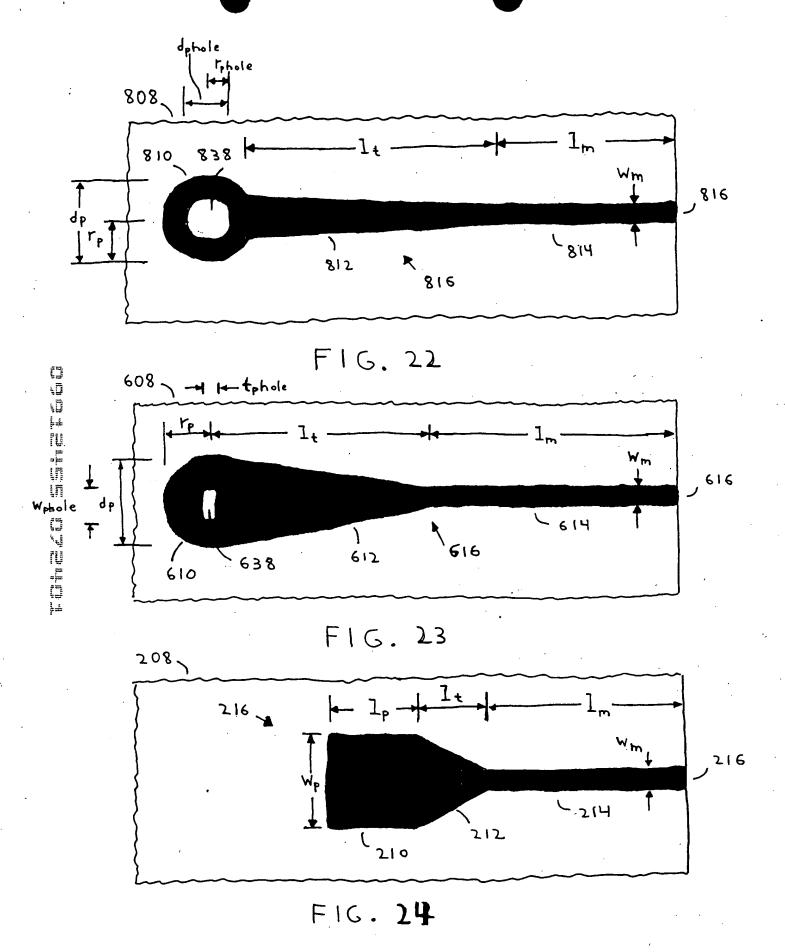


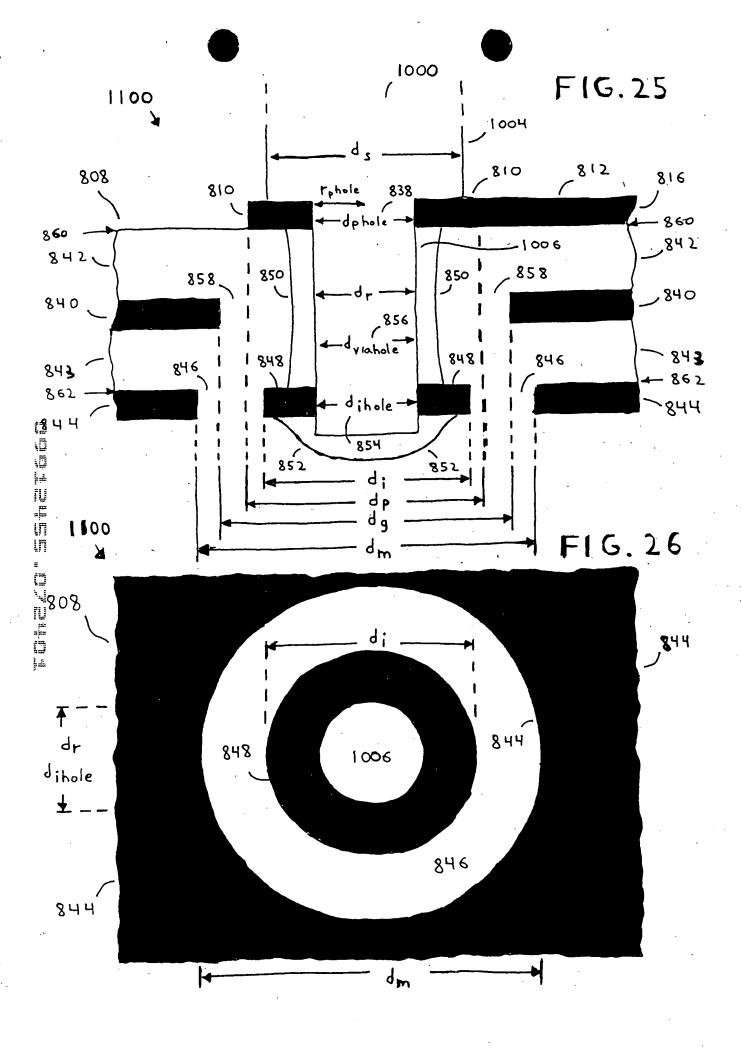


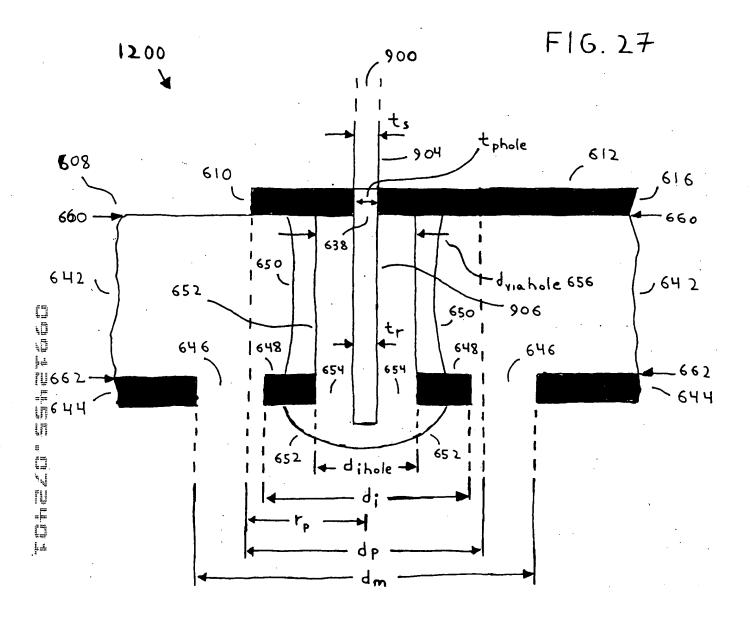
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**SMAO** 

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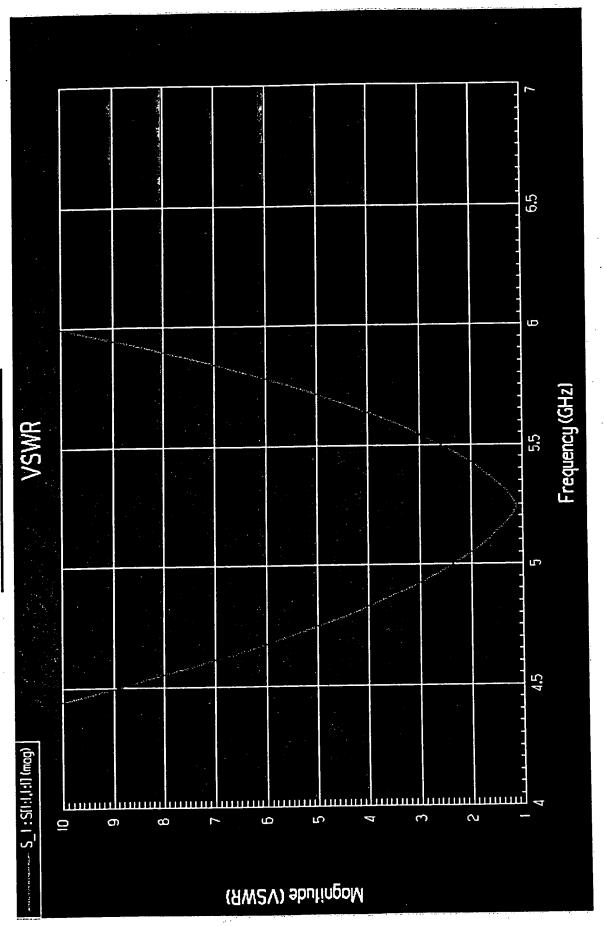
Input Impedance

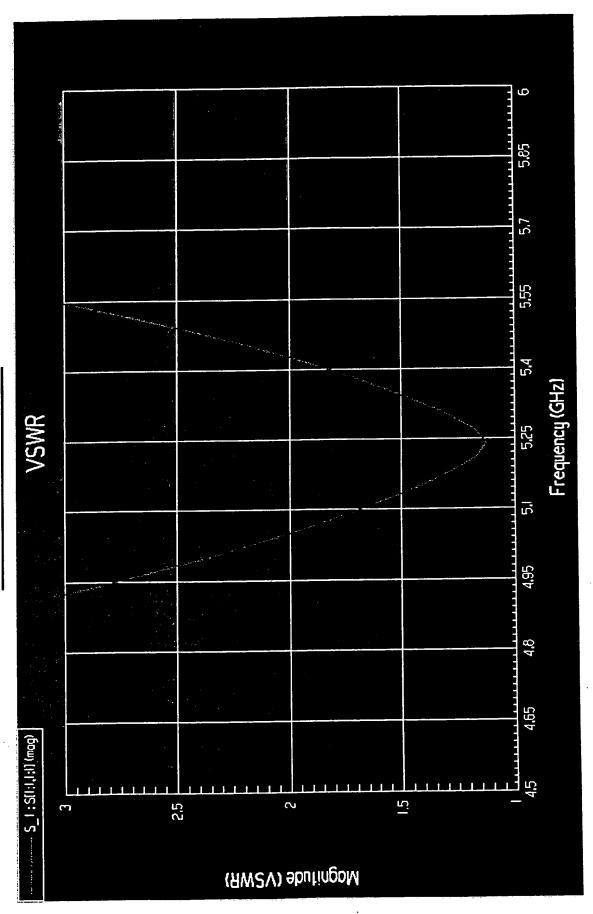
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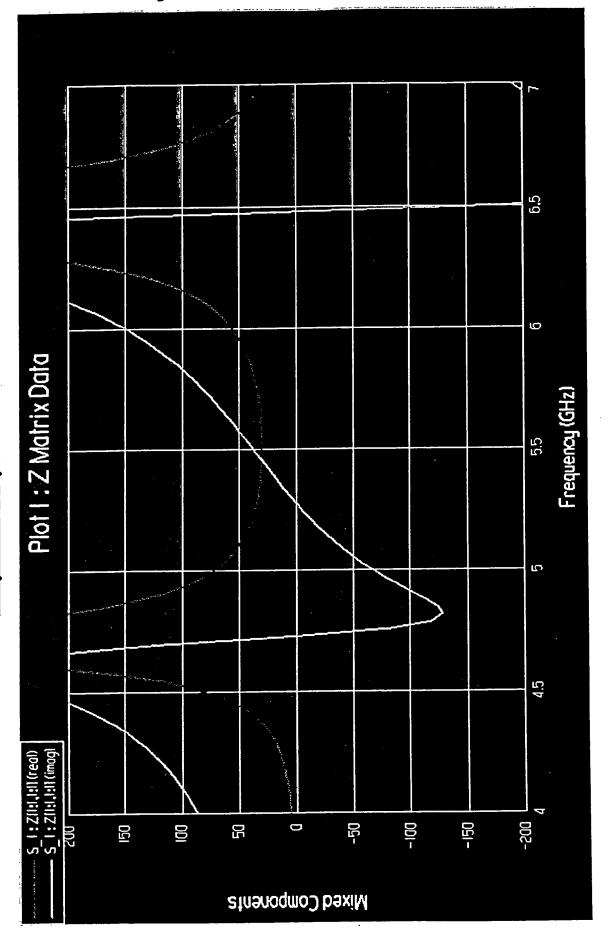
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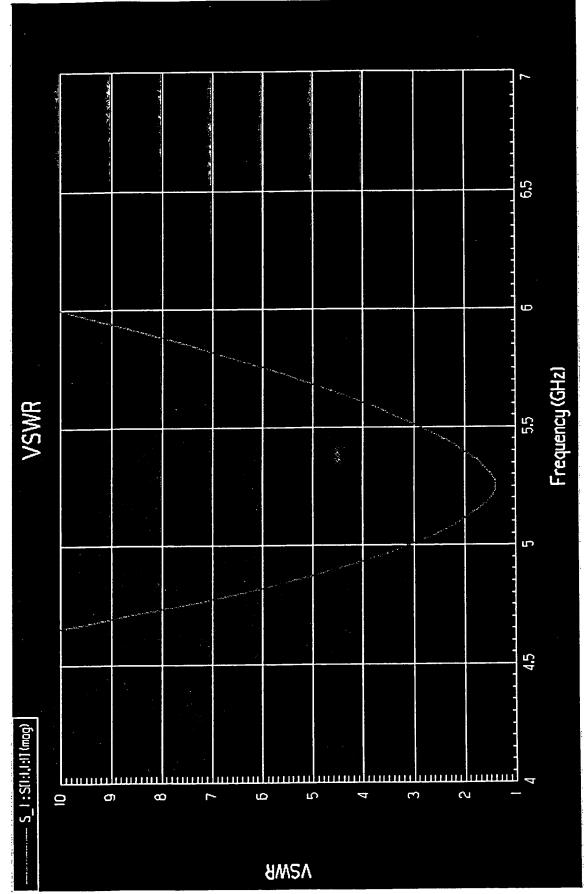


FIG. 33

Input Impedance

